

**WHAT IS CLAIMED IS:**

1. A radio receiver system for processing audio information transmitted by radio stations and discriminating distortions within the processed signals, comprising:

an antenna;

a frequency converter coupled to receive signals from the antenna; and

a digital receiver coupled to receive signals from the frequency converter and having

digital audio signals as outputs, the digital receiver comprising:

analog to digital converter circuitry;

demodulator circuitry coupled to receive digital signals from the analog to

digital converter circuitry and having demodulated digital signals as

outputs; and

distortion discrimination circuitry coupled to the demodulated digital signals

and having at least one distortion indication signal as an output, the

distortion indication signal indicating when a distortion event has been

detected.

2. The radio receiver system of claim 1, wherein the digital receiver further comprises a stereo decoder coupled to receive the demodulated digital signal from the demodulator and having decoded audio signals as outputs.

1 3. The radio receiver system of claim 2, wherein the stereo decoder receives the  
2 distortion indication signal and is responsive to the distortion indication signal to modify the  
3 audio output signals to diminish distortion effects in the decoded audio output signals.

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5 4. The radio receiver system of claim 1, wherein the distortion discrimination circuitry  
6 comprises impulse noise distortion discrimination circuitry and the distortion indication  
7 signal comprises an impulse noise distortion indication signal.

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9 5. The radio receiver system of claim 4, further comprising a stereo decoder coupled to  
10 receive the impulse noise distortion indication signal, wherein the stereo decoder is  
11 responsive to the impulse noise distortion indication signal to modify the audio output signals  
12 to diminish impulse noise distortion effects in the decoded audio output signals.

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14 6. The radio receiver system of claim 1, wherein the distortion discrimination circuitry  
15 comprises multipath distortion discrimination circuitry and the distortion indication signal  
16 comprises a multipath distortion indication signal.

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18 7. The radio receiver system of claim 6, further comprising a stereo decoder coupled to  
19 receive the multipath distortion indication signal, wherein the stereo decoder is responsive to  
20 the multipath distortion indication signal to modify the audio output signals to diminish  
21 multipath distortion effects in the decoded audio output signals.

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1 8. The radio receiver system of claim 6, further comprising another antenna and wherein  
2 the multipath distortion discrimination signal is utilized to switch between the antennas.

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4 9. A radio receiver for processing audio information and discriminating distortions  
5 within the processed signals, comprising

6 analog to digital converter circuitry;

7 demodulator circuitry coupled to receive digital signals from the analog to digital

8 converter circuitry and having demodulated digital signals as outputs; and

9 distortion discrimination circuitry coupled to the demodulated digital signals and

10 having at least one distortion indication signal as an output, the distortion

11 indication signal indicating when a distortion event has been detected.

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13 10. The radio receiver of claim 9, wherein the distortion discrimination circuitry  
14 comprises impulse noise distortion discrimination circuitry and the distortion indication  
15 signal comprises an impulse noise distortion indication signal.

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17 11. The radio receiver of claim 10, wherein the impulse noise discrimination circuitry  
18 monitors a digital magnitude signal and a digital multiplexed signal from the demodulator to  
19 determine the existence of an impulse noise event.

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21 12. The radio receiver of claim 11, wherein the demodulator is a CORDIC demodulator.

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1 13. The radio receiver of claim 11, wherein the impulse noise discrimination circuitry  
2 comprises:

3 a first threshold detection circuit coupled to receive the digital magnitude signal from  
4 the demodulator and having an output signal that is asserted when a threshold  
5 is exceeded; and

6 a second threshold detection circuit coupled to receive the digital multiplexed signal  
7 from the demodulator and having an output signal that is asserted when a  
8 threshold is exceeded; and

9 wherein a distortion event signal is asserted when both the output signals from the  
10 first and second threshold detection circuits are simultaneously asserted.  
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12 14. The radio receiver of claim 13, wherein the first and second threshold detection  
13 circuits each comprise a high-pass filter and a threshold comparator.

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15 15. The radio receiver of claim 14, wherein the high-pass filters have cut-off frequencies  
16 at about 100 kHz.  
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18 16. The radio receiver of claim 13, further comprising smoothing circuitry coupled to  
19 receive the distortion event signal and to produce an impulse output signal to act as the  
20 impulse noise distortion indication signal.  
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22 17. The radio receiver of claim 9, further comprising a stereo decoder coupled to receive  
23 the impulse noise distortion indication signal, wherein the stereo decoder is responsive to the

1 impulse noise distortion indication signal to modify the audio output signals to diminish  
2 impulse noise distortion effects in the decoded audio output signals.

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4 18. The radio receiver of claim 17, wherein the stereo decoder modifies the audio output  
5 signals by performing a blank and hold procedure.

6  
7 19. The radio receiver of claim 9, wherein the distortion discrimination circuitry  
8 comprises multipath distortion discrimination circuitry and the distortion indication signal  
9 comprises a multipath distortion indication signal.

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11 20. The radio receiver of claim 19, wherein the multipath discrimination circuitry  
12 monitors a magnitude signal from the demodulator to determine the existence of an impulse  
13 noise event.

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15 21. The radio receiver of claim 20, wherein the demodulator is a CORDIC demodulator.

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17 22. The radio receiver of claim 20, wherein the multipath discrimination circuitry  
18 comprises:

19 a first low-pass filter having a first time constant and being coupled to receive the

20 digital magnitude signal from the demodulator;

21 a second low-pass filter having a second time constant and being coupled to receive

22 the digital magnitude signal from the demodulator, the second time constant

23 being longer than the first time constant; and

1 compare circuitry coupled to receive output signals from the first and second low-pass  
2 filters and having the multipath distortion indication signal as an output, the  
3 multipath distortion indication signal being asserted if the output signals from  
4 the first and second low-pass filters differ in signal strength by more than a  
5 selected amount.  
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7 23. The automobile receiver of claim 19, further comprising a stereo decoder coupled to  
8 receive the multipath distortion indication signal, wherein the stereo decoder is responsive to  
9 the multipath distortion indication signal to modify the audio output signals to diminish  
10 multipath distortion effects in the decoded audio output signals.  
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12 24. The radio receiver of claim 23, wherein the stereo decoder modifies the audio output  
13 signals by performing a blank and hold procedure.  
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15 25. A method for discriminating distortion events within digital receivers, comprising:  
16 converting analog audio information to digital audio signal information;  
17 demodulating the digital audio signal information;  
18 discriminating distortions within the demodulated digital signals by monitoring the  
19 demodulated digital signals; and  
20 generating at least one distortion indication signal a distortion event has been detected  
21 within the demodulated digital signals.  
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1 26. The method of claim 25, wherein the discriminating distortions step comprises  
2 discriminating impulse noise distortion events and wherein the generating step comprises  
3 generating an impulse noise distortion indication signal.

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5 27. The method of claim 26, wherein the discriminating step comprises monitoring a  
6 digital magnitude signal and a digital multiplexed signal from the demodulator.

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8 28. The method of claim 27, wherein the discriminating step further comprises:  
9 asserting a first output signal if the digital magnitude signal from the demodulator  
10 exceeds a first threshold level;  
11 asserting a second output signal if the digital multiplexed signal from the demodulator  
12 exceeds a second threshold level; and  
13 asserting a distortion event signal when both the both first and second output signals  
14 are simultaneously asserted.

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16 29. The method of claim 28, wherein the first two asserting steps include filtering the  
17 digital magnitude signal and the digital multiplexed signal with high-pass filters having cut-  
18 off frequencies at about 100 kHz.

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20 30. The method of claim 28, further comprising generating the impulse noise distortion  
21 indication signal by smoothing the distortion event signal.

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1 31. The method of claim 25, further comprising modifying the audio output signals in  
2 response to the impulse noise distortion indication signal to diminish impulse noise distortion  
3 effects in the audio output signals.

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5 32. The method of claim 25, wherein the discriminating distortion step comprises  
6 discriminating multipath distortion events and wherein the generating step comprises  
7 generating a multipath distortion indication signal.

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9 33. The method of claim 32, wherein the discriminating step comprises monitoring a  
10 magnitude signal from the demodulator.

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12 34. The method of claim 33, wherein the discriminating step further comprises:  
13 filtering the digital magnitude signal from the demodulator with a first filter having a  
14 first time constant;  
15 filtering the digital magnitude signal from the demodulator with a second filter having  
16 a second time constant, the second time constant being longer than the first  
17 time constant;  
18 comparing the output signals from the first and second filters; and  
19 asserting the multipath distortion indication signal if the output signals from the first  
20 and second filters differ in signal strength by more than a selected amount.  
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1 35. The method of claim 32, further comprising modifying the audio output signals in  
2 response to the impulse noise distortion indication signal to diminish impulse noise distortion  
3 effects in the audio output signals.  
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5 36. Distortion discrimination circuitry for a radio receiver, comprising  
6 demodulated digital audio input signals; and  
7 at least one distortion indication signal as an output, the distortion indication output  
8 signal indicating when a distortion event has been detected in the demodulated  
9 digital audio input signals.  
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11 37. The distortion discrimination circuitry of claim 36, wherein the distortion  
12 discrimination circuitry comprises impulse noise distortion discrimination circuitry and the  
13 distortion indication signal comprises an impulse noise distortion indication signal.  
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15 38. The distortion discrimination circuitry of claim 37, wherein the demodulated digital  
16 audio input signals comprise a digital magnitude signal and a digital multiplexed signal from  
17 a demodulator and the impulse noise distortion indication signal is based upon monitoring  
18 these two signals to determine the existence of an impulse noise event.  
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20 39. The distortion discrimination circuitry of claim 38, further comprising:  
21 a first threshold detection circuit coupled to receive the digital magnitude signal from  
22 the demodulator and having an output signal that is asserted when a threshold  
23 is exceeded; and

1 a second threshold detection circuit coupled to receive the digital multiplexed signal  
2 from the demodulator and having an output signal that is asserted when a  
3 threshold is exceeded; and  
4 wherein a distortion event signal is asserted when both the output signals from the  
5 first and second threshold detection circuits are simultaneously asserted.  
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7 40. The distortion discrimination circuitry of claim 39, wherein the first and second  
8 threshold detection circuits each comprise a high-pass filter and a threshold comparator.

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10 41. The distortion discrimination circuitry of claim 39, further comprising smoothing  
11 circuitry coupled to receive the distortion event signal and to produce an impulse output  
12 signal to act as the impulse noise distortion indication signal.  
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14 42. The distortion discrimination circuitry of claim 36, wherein the distortion  
15 discrimination circuitry comprises multipath distortion discrimination circuitry and the  
16 distortion indication signal comprises a multipath distortion indication signal.  
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18 43. The distortion discrimination circuitry of claim 42, wherein the demodulated digital  
19 audio input signals comprise a magnitude signal from the demodulator and the multipath  
20 distortion indication signal is based upon monitoring this signal to determine the existence of  
21 an impulse noise event.  
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23 44. The distortion discrimination circuitry of claim 43, further comprising:

1 a first low-pass filter having a first time constant and being coupled to receive the  
2 digital magnitude signal from the demodulator;  
3 a second low-pass filter having a second time constant and being coupled to receive  
4 the digital magnitude signal from the demodulator, the second time constant  
5 being longer than the first time constant; and  
6 compare circuitry coupled to receive output signals from the first and second low-pass  
7 filters and having the multipath distortion indication signal as an output, the  
8 multipath distortion indication signal being asserted if the output signals from  
9 the first and second low-pass filters differ in signal strength by more than a  
10 selected amount.  
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